

# Ian A. Cleary

Phoenix, AZ 85007 ♦ US Citizen  
[contact@iancleary.me](mailto:contact@iancleary.me) ♦ [icanclearynow.com](http://icanclearynow.com)

## EXPERIENCE

---

### ViaSat Advanced Microwave Products

Tempe, AZ

Systems Engineer – Satellite Payloads

September 2017-Present

- Co-developed the system test plan for our Viasat-3 Satellite Payloads
- Verified requirements and tracked compliance using Python (pandas, scikit-rf, numpy, plotly), Jupyter, and Docker
- Authored predictive models of system performance from module level test data, reallocated specs to improve schedule

### ViaSat Advanced Microwave Products

Tempe, AZ

RF Engineer – Circuit Board Designer

November 2014-November 2017

- Designed wire bond interfaces for amplifiers and mixers in the 20-30 GHz range on a Rogers PCB Substrate
- Iterated a 3-stage 20 GHz Mitsubishi LNA Amplifier and Hairpin filters design onto a new Rogers Substrate
- Implemented reference designators for wire bond to improve quality in the design, test, and manufacturing processes

### ViaSat Advanced Microwave Products

Gilbert, AZ

RF Engineer – SiGe/GaN/GaAs Designer

July 2013-November 2014

- Analyzed data and conducted tests diagnosing Ka band radar SiGe transceiver chip in the Jazz SBC18HA process
- Designed and simulated Ku band LO Poly-Phase Filter in a TriQuint GaAs process

### Lockheed Martin Space Systems Company

Sunnyvale, CA

Systems Engineering Intern – THAAD Program Electronics

May-August 2012

- Analyzed data and conducted tests for a RF curl antenna/coupler, ignition systems, and a Schmitt crystal oscillator
- Efficiently communicated technical analyses to customers, sub-contractors, management, and subject matter experts

## EDUCATION

---

### University of Michigan

Ann Arbor, MI

B.S.E in Electrical Engineering, GPA: 3.228/4.000

April 2013

Courses: Analog Integrated Circuits, Microwave Circuits I, Monolithic Amplifier Circuits, Electromagnetics II, Digital Signal Processing, Probabilistic Methods in Engineering, Analog Circuits, Semiconductor Device Physics, Intro to Logic Design, Programming & Data Structures, Intro to Thermodynamics, Economic Decision Making, Financial Accounting

### Arizona State University

Tempe, AZ

M.S.E Electrical Engineering (Electromagnetics, antennas and microwave circuits)

Spring 2017

## AWARDS

---

William J. Branstrom Freshman Prize (Awarded to top 5% of freshman class)

Fall 2009

Honor List

Fall 2009, Fall 2012, Spring 2013

## COMPUTER SKILLS

---

Proficient: Jama Connect, Python, Jupyter, Docker

Intermediate: Vue.js, Visio, SysML, Agilent ADS, HFSS, FastAPI, Nuxt.js

## ACTIVITIES

---

Underwater Hockey, Hiking, Yoga, Computer Games

System Administer to my Neighborhood Communities Website

Self-Hosting Infrastructure when practical

Cooks an excellent Orzo al Limone

Listening to podcasts and audiobooks