

Ian A. Cleary

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EXPERIENCE

ViaSat Space and Commercial Networks

Tempe, AZ

Digital Subsystem Lead – Viasat-4 Satellite Payloads

August 2021-Present

- Finished subsystem specifications, completed wire lists, and maintained budgets
- Allocated budgets and tracked compliance using Jama Connect requirements, traceability, and test plan tooling
- Co-maintainer of command and telemetry database used during spacecraft integration and test, in-orbit-test, and mission operations

ViaSat Space and Commercial Networks

Tempe, AZ

Systems Engineer – Viasat-3 Satellite Payloads

September 2017-Present

- Co-developed the system test plan for our Viasat-3 Satellite Payloads
- Verified requirements and tracked compliance using Python (pandas, scikit-rl, numpy, plotly), Jupyter, and Docker
- Authored predictive models of system performance from module level test data, reallocated specs to improve schedule

ViaSat Advanced Microwave Products

Tempe, AZ

RF Engineer – Circuit Board Designer

November 2014-November 2017

- Designed wire bond interfaces for amplifiers and mixers in the 20-30 GHz range on a Rogers PCB Substrate
- Iterated a 3-stage 20 GHz Mitsubishi LNA Amplifier and Hairpin filters design onto a new Rogers Substrate
- Implemented reference designators for wire bond to improve quality in the design, test, and manufacturing processes

ViaSat Advanced Microwave Products

Gilbert, AZ

RF Engineer – SiGe/GaN/GaAs Designer

July 2013-November 2014

- Analyzed data and conducted tests diagnosing Ka band radar SiGe transceiver chip in the Jazz SBC18HA process
- Designed and simulated Ku band LO Poly-Phase Filter in a TriQuint GaAs process

Lockheed Martin Space Systems Company

Sunnyvale, CA

Systems Engineering Intern – THAAD Program Electronics

May-August 2012

- Analyzed data and conducted tests for a RF curl antenna/coupler, ignition systems, and a Schmitt crystal oscillator
- Efficiently communicated technical analyses to customers, sub-contractors, management, and subject matter experts

EDUCATION

University of Michigan

Ann Arbor, MI

B.S.E in Electrical Engineering

April 2013

Courses: Analog Integrated Circuits, Microwave Circuits I, Monolithic Amplifier Circuits, Electromagnetics II, Digital Signal Processing, Probabilistic Methods in Engineering, Analog Circuits, Semiconductor Device Physics, Intro to Logic Design, Programming & Data Structures, Intro to Thermodynamics, Economic Decision Making, Financial Accounting

Arizona State University

Tempe, AZ

M.S.E Electrical Engineering (Electromagnetics, Wireless Communications, Systems Engineering)

Spring 2017

AWARDS

Model Based Systems Engineering Fundamentals Certificate Program

December, 2022

William J. Branstrom Freshman Prize (Awarded to top 5% of freshman class)

Fall 2009

ACTIVITIES

Underwater Hockey, Hiking, Yoga; Computer Games; self-hosting infrastructure with UnRaid and Tailscale; open-source advocate (<https://github.com/iancleary>); cooks an excellent Orzo al Limone; listening to podcasts and audiobooks